



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R13/R16) I Semester Regular/Supplementary Examinations December 2016

College: MOTHER THERESA EDUCATIONAL SOCIETY GROUP OF INSTITUTIONS, VISSANNAPET:9T

Discrepancy pertaining to these results are to be submitted on or before 19-07-2017 with following documents at CE(PG) Office, JNTUK, Kakinada

- Online Registration Proof
- Hallticket
- DForm(Online)
- DForm(Offline)
- Attendance Sheet
- Any Other supporting Documents

Htno	Subcode	Subname	Internal	External	credits
139T1D0522	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	36	32	1
139T1D5805	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	36	16	0
149T1D2110	G2102	ADVANCED THERMODYNAMICS	33	30	1
149T1D3802	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	32	14	0
149T1D3808	G3801	OPTICAL COMMUNICATION TECHNOLOGY	35	5	0
149T1D3815	G3801	OPTICAL COMMUNICATION TECHNOLOGY	37	13	0
149T1D3815	G6806	DIGITAL SYSTEM DESIGN	36	-1	0
149T1D5611	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	36	31	1
149T1D5612	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	35	-1	0
149T1D5618	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	25	1
149T1D5624	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	35	35	1
149T1D5807	G0503	DATA BASE MANAGEMENT SYSTEMS	38	-1	0
149T1D5807	G0505	SOFTWARE ENGINEERING	32	12	0
149T1D5815	G0503	DATA BASE MANAGEMENT SYSTEMS	36	26	1
149T1D5818	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	-1	0
149T1D7204	G5701	DIGITAL DESIGN USING HDL	32	25	1
149T1D7204	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	26	1
149T1D7209	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	33	1
149T1D7209	G6809	CMOS DIGITAL IC DESIGN	35	14	0
149T1D8704	G8701	THEORY OF ELASTICITY	33	20	0
149T1D8708	G8702	MATRIX ANALYSIS OF STRUCTURES	31	6	0
149T1D8709	G8701	THEORY OF ELASTICITY	30	-1	0
149T1D8709	G8705	SUB-STRUCTURE DESIGN	33	28	1
149T1D8716	G8705	SUB-STRUCTURE DESIGN	37	38	1
149T1D8718	G2201	APPLIED MATHEMATICS	36	6	0
149T1D8719	G8701	THEORY OF ELASTICITY	30	29	1
149T1D8719	G8705	SUB-STRUCTURE DESIGN	35	9	0
149T1D8721	G2201	APPLIED MATHEMATICS	36	37	1
159T1D0501	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	37	27	1
159T1D0501	G0509	DATA COMMUNICATIONS AND COMPUTER NETWORK	37	5	0
159T1D0503	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	15	0
159T1D0503	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	36	24	1
159T1D0503	G0509	DATA COMMUNICATIONS AND COMPUTER NETWORK	35	-1	0
159T1D2102	G2103	ADVANCED HEAT & MASS TRANSFER	35	27	1

Htno	Subcode	Subname	Internal	External	credits
159T1D2103	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	35	11	0
159T1D2103	G2103	ADVANCED HEAT & MASS TRANSFER	34	25	1
159T1D2103	G2104	ADVANCED FLUID MECHANICS	34	31	1
159T1D2104	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	34	24	1
159T1D2104	G2102	ADVANCED THERMODYNAMICS	36	31	1
159T1D2104	G2103	ADVANCED HEAT & MASS TRANSFER	36	24	1
159T1D2104	G2104	ADVANCED FLUID MECHANICS	37	0	0
159T1D2104	G2107	RENEWABLE ENERGY TECHNOLOGIES	36	15	0
159T1D2104	G2113	THERMAL ENGINEERING LAB	35	55	1
159T1D2105	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	37	13	0
159T1D2105	G2102	ADVANCED THERMODYNAMICS	36	24	1
159T1D2105	G2103	ADVANCED HEAT & MASS TRANSFER	35	32	1
159T1D2106	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	35	21	0
159T1D2106	G2104	ADVANCED FLUID MECHANICS	37	0	0
159T1D2108	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	37	28	1
159T1D2108	G2102	ADVANCED THERMODYNAMICS	36	37	1
159T1D2108	G2104	ADVANCED FLUID MECHANICS	37	0	0
159T1D2111	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	38	30	1
159T1D2111	G2102	ADVANCED THERMODYNAMICS	36	17	0
159T1D2111	G2104	ADVANCED FLUID MECHANICS	34	14	0
159T1D2111	G2107	RENEWABLE ENERGY TECHNOLOGIES	36	29	1
159T1D2112	G2102	ADVANCED THERMODYNAMICS	36	31	1
159T1D2112	G2107	RENEWABLE ENERGY TECHNOLOGIES	38	36	1
159T1D2113	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	38	-1	0
159T1D2113	G2102	ADVANCED THERMODYNAMICS	38	-1	0
159T1D2113	G2103	ADVANCED HEAT & MASS TRANSFER	37	-1	0
159T1D2115	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	36	20	0
159T1D2116	G2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	35	22	0
159T1D2116	G2103	ADVANCED HEAT & MASS TRANSFER	37	24	1
159T1D2116	G2104	ADVANCED FLUID MECHANICS	34	0	0
159T1D3801	G3801	OPTICAL COMMUNICATION TECHNOLOGY	37	37	1
159T1D3801	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	37	27	1
159T1D3801	G6806	DIGITAL SYSTEM DESIGN	37	19	0
159T1D3801	G8205	DETECTION & ESTIMATION THEORY	38	27	1
159T1D3805	G3801	OPTICAL COMMUNICATION TECHNOLOGY	36	11	0
159T1D3805	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	37	16	0
159T1D3805	G8205	DETECTION & ESTIMATION THEORY	37	32	1
159T1D3807	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	39	16	0
159T1D3807	G6802	VLSI TECHNOLOGY AND DESIGN	38	34	1
159T1D3809	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	36	38	1
159T1D3809	G6806	DIGITAL SYSTEM DESIGN	36	31	1
159T1D3812	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	37	15	0
159T1D3812	G6806	DIGITAL SYSTEM DESIGN	38	21	0
159T1D3816	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	38	8	0
159T1D3817	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	35	24	1
159T1D3817	G6806	DIGITAL SYSTEM DESIGN	33	35	1
159T1D5602	G5603	POWER SYSTEM OPERATION AND CONTROL	36	36	1
159T1D5603	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	38	20	0
159T1D5603	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	37	33	1
159T1D5604	G5601	MICROPROCESSORS & MICROCONTROLLERS	39	41	1
159T1D5604	G5602	HVDC TRANSMISSION	35	30	1

Htno	Subcode	Subname	Internal	External	credits
159T1D5604	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	38	35	1
159T1D5605	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	39	24	1
159T1D5606	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	36	16	0
159T1D5606	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	39	25	1
159T1D5607	G5602	HVDC TRANSMISSION	34	30	1
159T1D5608	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	39	35	1
159T1D5611	G5602	HVDC TRANSMISSION	36	30	1
159T1D5611	G5603	POWER SYSTEM OPERATION AND CONTROL	36	27	1
159T1D5611	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	35	27	1
159T1D5611	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	38	25	1
159T1D5611	G5611	ADVANCED DIGITAL SIGNAL PROCESSING	0	-1	0
159T1D5611	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	37	35	1
159T1D5612	G5602	HVDC TRANSMISSION	38	28	1
159T1D5612	G5603	POWER SYSTEM OPERATION AND CONTROL	35	24	1
159T1D5612	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	36	37	1
159T1D5612	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	33	28	1
159T1D5613	G5602	HVDC TRANSMISSION	36	28	1
159T1D5613	G5603	POWER SYSTEM OPERATION AND CONTROL	35	28	1
159T1D5613	G5615	SIMULATION LAB	37	55	1
159T1D5614	G5601	MICROPROCESSORS & MICROCONTROLLERS	37	-1	0
159T1D5614	G5602	HVDC TRANSMISSION	33	-1	0
159T1D5614	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	38	-1	0
159T1D5615	G5602	HVDC TRANSMISSION	36	27	1
159T1D5615	G5603	POWER SYSTEM OPERATION AND CONTROL	37	19	0
159T1D5615	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	38	29	1
159T1D5620	G5603	POWER SYSTEM OPERATION AND CONTROL	36	29	1
159T1D5620	G5612	GENERATION & MEASUREMENT OF HIGH VOLTAGE	37	24	1
159T1D5802	G0503	DATA BASE MANAGEMENT SYSTEMS	38	18	0
159T1D5811	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	21	0
159T1D5811	G0503	DATA BASE MANAGEMENT SYSTEMS	36	28	1
159T1D5813	G0503	DATA BASE MANAGEMENT SYSTEMS	36	24	1
159T1D5813	G0504	OPERATING SYSTEM	35	24	1
159T1D5814	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	35	-1	0
159T1D5814	G0503	DATA BASE MANAGEMENT SYSTEMS	38	8	0
159T1D5814	G0504	OPERATING SYSTEM	33	8	0
159T1D5814	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	18	0
159T1D7201	G5701	DIGITAL DESIGN USING HDL	36	-1	0
159T1D7201	G6802	VLSI TECHNOLOGY AND DESIGN	34	-1	0
159T1D7201	G6803	CMOS ANALOG IC DESIGN	36	-1	0
159T1D7201	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	-1	0
159T1D7201	G6806	DIGITAL SYSTEM DESIGN	34	-1	0
159T1D7201	G6809	CMOS DIGITAL IC DESIGN	34	-1	0
159T1D7201	G6811	VLSI LABORATORY -I	34	-1	0
159T1D7202	G6802	VLSI TECHNOLOGY AND DESIGN	35	37	1
159T1D7202	G6806	DIGITAL SYSTEM DESIGN	36	33	1
159T1D7202	G6811	VLSI LABORATORY -I	35	50	1
159T1D7206	G5701	DIGITAL DESIGN USING HDL	36	24	1
159T1D7206	G6802	VLSI TECHNOLOGY AND DESIGN	36	33	1
159T1D7206	G6803	CMOS ANALOG IC DESIGN	34	31	1
159T1D7206	G6806	DIGITAL SYSTEM DESIGN	34	28	1
159T1D7210	G5701	DIGITAL DESIGN USING HDL	31	35	1

Htno	Subcode	Subname	Internal	External	credits
159T1D7210	G6802	VLSI TECHNOLOGY AND DESIGN	33	40	1
159T1D7210	G6806	DIGITAL SYSTEM DESIGN	33	19	0
159T1D7210	G6809	CMOS DIGITAL IC DESIGN	33	20	0
159T1D7211	G5701	DIGITAL DESIGN USING HDL	35	28	1
159T1D7211	G6802	VLSI TECHNOLOGY AND DESIGN	36	24	1
159T1D7211	G6803	CMOS ANALOG IC DESIGN	35	24	1
159T1D7211	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	27	1
159T1D7211	G6806	DIGITAL SYSTEM DESIGN	33	12	0
159T1D7211	G6809	CMOS DIGITAL IC DESIGN	35	27	1
159T1D7213	G6802	VLSI TECHNOLOGY AND DESIGN	36	37	1
159T1D7213	G6806	DIGITAL SYSTEM DESIGN	36	33	1
159T1D7214	G6806	DIGITAL SYSTEM DESIGN	36	21	0
159T1D7215	G6806	DIGITAL SYSTEM DESIGN	35	28	1
159T1D7217	G6802	VLSI TECHNOLOGY AND DESIGN	35	19	0
159T1D7217	G6803	CMOS ANALOG IC DESIGN	36	31	1
159T1D7217	G6809	CMOS DIGITAL IC DESIGN	35	19	0
159T1D8702	G2201	APPLIED MATHEMATICS	38	-1	0
159T1D8702	G8702	MATRIX ANALYSIS OF STRUCTURES	37	-1	0
159T1D8702	G8707	REPAIR AND REHABILITATION OF STRUCTURES	37	-1	0
159T1D8703	G8703	STRUCTURAL DYNAMICS	38	27	1
159T1D8706	G8701	THEORY OF ELASTICITY	39	8	0
159T1D8706	G8703	STRUCTURAL DYNAMICS	39	-1	0
159T1D8706	G8707	REPAIR AND REHABILITATION OF STRUCTURES	38	15	0
159T1D8707	G2201	APPLIED MATHEMATICS	38	13	0
159T1D8707	G8701	THEORY OF ELASTICITY	39	25	1
159T1D8707	G8702	MATRIX ANALYSIS OF STRUCTURES	39	7	0
159T1D8707	G8703	STRUCTURAL DYNAMICS	37	24	1
159T1D8707	G8705	SUB-STRUCTURE DESIGN	39	19	0
159T1D8707	G8707	REPAIR AND REHABILITATION OF STRUCTURES	39	31	1
159T1D8709	G2201	APPLIED MATHEMATICS	36	7	0
159T1D8709	G8701	THEORY OF ELASTICITY	37	5	0
159T1D8709	G8702	MATRIX ANALYSIS OF STRUCTURES	37	6	0
159T1D8709	G8703	STRUCTURAL DYNAMICS	37	4	0
159T1D8709	G8705	SUB-STRUCTURE DESIGN	37	10	0
159T1D8709	G8707	REPAIR AND REHABILITATION OF STRUCTURES	37	6	0
159T1D8710	G2201	APPLIED MATHEMATICS	38	0	0
159T1D8710	G8701	THEORY OF ELASTICITY	38	16	0
159T1D8710	G8702	MATRIX ANALYSIS OF STRUCTURES	38	0	0
159T1D8710	G8703	STRUCTURAL DYNAMICS	37	24	1
159T1D8710	G8705	SUB-STRUCTURE DESIGN	39	8	0
159T1D8710	G8707	REPAIR AND REHABILITATION OF STRUCTURES	37	31	1
159T1D8711	G8703	STRUCTURAL DYNAMICS	39	39	1
159T1D8717	G8703	STRUCTURAL DYNAMICS	39	-1	0
159T1D8717	G8707	REPAIR AND REHABILITATION OF STRUCTURES	38	-1	0
169T1D2101	I2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	34	24	1
169T1D2101	I2102	ADVANCED THERMODYNAMICS	36	33	1
169T1D2101	I2103	ADVANCED HEAT TRANSFER	36	25	1
169T1D2101	I2104	ADVANCED FLUID MECHANICS	35	34	1
169T1D2101	I2107	RENEWABLE ENERGY TECHNOLOGIES ELECTIVE	35	28	1
169T1D2101	I2110	SOLAR ENERGY TECHNOLOGY ELECTIVE 2	34	45	1
169T1D2101	I2113	THERMAL ENGINEERING LAB	33	57	1

Htno	Subcode	Subname	Internal	External	credits
169T1D2102	I2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	35	30	1
169T1D2102	I2102	ADVANCED THERMODYNAMICS	36	27	1
169T1D2102	I2103	ADVANCED HEAT TRANSFER	36	17	0
169T1D2102	I2104	ADVANCEDDD FLUID MECAHNICS	37	27	1
169T1D2102	I2107	RENEWABLE ENERGY TECHNOLOGIES ELECTIVE	37	27	1
169T1D2102	I2110	SOLAR ENERGY TECHNOLOGY ELECTIVE 2	35	39	1
169T1D2102	I2113	THERMAL ENGINEERING LAB	34	57	1
169T1D2103	I2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	34	30	1
169T1D2103	I2102	ADVANCED THERMODYNAMICS	37	9	0
169T1D2103	I2103	ADVANCED HEAT TRANSFER	35	26	1
169T1D2103	I2104	ADVANCEDDD FLUID MECAHNICS	34	0	0
169T1D2103	I2107	RENEWABLE ENERGY TECHNOLOGIES ELECTIVE	34	25	1
169T1D2103	I2110	SOLAR ENERGY TECHNOLOGY ELECTIVE 2	35	36	1
169T1D2103	I2113	THERMAL ENGINEERING LAB	33	56	1
169T1D2104	I2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	34	-1	0
169T1D2104	I2102	ADVANCED THERMODYNAMICS	36	-1	0
169T1D2104	I2103	ADVANCED HEAT TRANSFER	34	-1	0
169T1D2104	I2104	ADVANCEDDD FLUID MECAHNICS	35	-1	0
169T1D2104	I2107	RENEWABLE ENERGY TECHNOLOGIES ELECTIVE	35	-1	0
169T1D2104	I2110	SOLAR ENERGY TECHNOLOGY ELECTIVE 2	35	-1	0
169T1D2104	I2113	THERMAL ENGINEERING LAB	33	55	1
169T1D2105	I2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	37	32	1
169T1D2105	I2102	ADVANCED THERMODYNAMICS	38	34	1
169T1D2105	I2103	ADVANCED HEAT TRANSFER	37	27	1
169T1D2105	I2104	ADVANCEDDD FLUID MECAHNICS	35	17	0
169T1D2105	I2107	RENEWABLE ENERGY TECHNOLOGIES ELECTIVE	36	38	1
169T1D2105	I2110	SOLAR ENERGY TECHNOLOGY ELECTIVE 2	36	46	1
169T1D2105	I2113	THERMAL ENGINEERING LAB	35	55	1
169T1D2107	I2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	34	-1	0
169T1D2107	I2102	ADVANCED THERMODYNAMICS	33	33	1
169T1D2107	I2103	ADVANCED HEAT TRANSFER	34	32	1
169T1D2107	I2104	ADVANCEDDD FLUID MECAHNICS	35	50	1
169T1D2107	I2107	RENEWABLE ENERGY TECHNOLOGIES ELECTIVE	33	34	1
169T1D2107	I2110	SOLAR ENERGY TECHNOLOGY ELECTIVE 2	33	46	1
169T1D2107	I2113	THERMAL ENGINEERING LAB	34	-1	0
169T1D3801	I3801	SYSTEM DESIGN AND DATA COMMUNICATIONS LA	38	57	1
169T1D3801	I4503	ADVANCED DIGITAL SIGNAL PROCESSING	35	36	1
169T1D3801	I4504	DIGITAL DATA COMMUNICATIONS	37	32	1
169T1D3801	I6801	DIGITAL SYSTEM DESIGN	35	40	1
169T1D3801	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	28	1
169T1D3801	I8205	DETECTION AND ESTIMATION THEORY	35	36	1
169T1D3801	I8206	OPTICAL COMMUNICATION TECHNOLOGY ELECTIV	36	46	1
169T1D3802	I3801	SYSTEM DESIGN AND DATA COMMUNICATIONS LA	36	54	1
169T1D3802	I4503	ADVANCED DIGITAL SIGNAL PROCESSING	33	-1	0
169T1D3802	I4504	DIGITAL DATA COMMUNICATIONS	36	-1	0
169T1D3802	I6801	DIGITAL SYSTEM DESIGN	35	-1	0
169T1D3802	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	-1	0
169T1D3802	I8205	DETECTION AND ESTIMATION THEORY	34	-1	0
169T1D3802	I8206	OPTICAL COMMUNICATION TECHNOLOGY ELECTIV	35	-1	0
169T1D3803	I3801	SYSTEM DESIGN AND DATA COMMUNICATIONS LA	39	58	1
169T1D3803	I4503	ADVANCED DIGITAL SIGNAL PROCESSING	37	10	0

Htno	Subcode	Subname	Internal	External	credits
169T1D3803	I4504	DIGITAL DATA COMMUNICATIONS	38	25	1
169T1D3803	I6801	DIGITAL SYSTEM DESIGN	37	41	1
169T1D3803	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	24	1
169T1D3803	I8205	DETECTION AND ESTIMATION THEORY	37	30	1
169T1D3803	I8206	OPTICAL COMMUNICATION TECHNOLOGY ELECTIV	37	26	1
169T1D5602	I5601	MICROPROCESSORS & MICROCONTROLLERS	37	9	0
169T1D5602	I5602	HVDC TRANSMISSION	37	24	1
169T1D5602	I5603	POWER SYSTEM OPERATION AND CONTROL	38	25	1
169T1D5602	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	34	24	1
169T1D5602	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	31	1
169T1D5602	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	38	24	1
169T1D5602	I5615	SIMULATION LABORATORY	39	57	1
169T1D5603	I5601	MICROPROCESSORS & MICROCONTROLLERS	35	14	0
169T1D5603	I5602	HVDC TRANSMISSION	35	27	1
169T1D5603	I5603	POWER SYSTEM OPERATION AND CONTROL	36	13	0
169T1D5603	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	29	26	1
169T1D5603	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	38	27	1
169T1D5603	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	36	36	1
169T1D5603	I5615	SIMULATION LABORATORY	30	59	1
169T1D5604	I5601	MICROPROCESSORS & MICROCONTROLLERS	36	-1	0
169T1D5604	I5602	HVDC TRANSMISSION	35	-1	0
169T1D5604	I5603	POWER SYSTEM OPERATION AND CONTROL	36	-1	0
169T1D5604	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	33	-1	0
169T1D5604	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	38	-1	0
169T1D5604	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	37	-1	0
169T1D5604	I5615	SIMULATION LABORATORY	38	55	1
169T1D5605	I5601	MICROPROCESSORS & MICROCONTROLLERS	36	24	1
169T1D5605	I5602	HVDC TRANSMISSION	37	30	1
169T1D5605	I5603	POWER SYSTEM OPERATION AND CONTROL	38	34	1
169T1D5605	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	32	1
169T1D5605	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	50	1
169T1D5605	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	37	39	1
169T1D5605	I5615	SIMULATION LABORATORY	35	58	1
169T1D5606	I5601	MICROPROCESSORS & MICROCONTROLLERS	38	28	1
169T1D5606	I5602	HVDC TRANSMISSION	36	30	1
169T1D5606	I5603	POWER SYSTEM OPERATION AND CONTROL	39	24	1
169T1D5606	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	36	25	1
169T1D5606	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	39	25	1
169T1D5606	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	38	37	1
169T1D5606	I5615	SIMULATION LABORATORY	38	59	1
169T1D5607	I5601	MICROPROCESSORS & MICROCONTROLLERS	33	14	0
169T1D5607	I5602	HVDC TRANSMISSION	32	21	0
169T1D5607	I5603	POWER SYSTEM OPERATION AND CONTROL	30	7	0
169T1D5607	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	21	29	1
169T1D5607	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	27	18	0
169T1D5607	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	24	26	1
169T1D5607	I5615	SIMULATION LABORATORY	30	57	1
169T1D5608	I5601	MICROPROCESSORS & MICROCONTROLLERS	36	14	0
169T1D5608	I5602	HVDC TRANSMISSION	36	19	0
169T1D5608	I5603	POWER SYSTEM OPERATION AND CONTROL	37	17	0
169T1D5608	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	24	1

Htno	Subcode	Subname	Internal	External	credits
169T1D5608	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	38	20	0
169T1D5608	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	37	24	1
169T1D5608	I5615	SIMULATION LABORATORY	37	57	1
169T1D5609	I5601	MICROPROCESSORS & MICROCONTROLLERS	39	27	1
169T1D5609	I5602	HVDC TRANSMISSION	36	25	1
169T1D5609	I5603	POWER SYSTEM OPERATION AND CONTROL	38	24	1
169T1D5609	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	16	0
169T1D5609	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	39	18	0
169T1D5609	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	39	30	1
169T1D5609	I5615	SIMULATION LABORATORY	38	58	1
169T1D5610	I5601	MICROPROCESSORS & MICROCONTROLLERS	37	7	0
169T1D5610	I5602	HVDC TRANSMISSION	36	16	0
169T1D5610	I5603	POWER SYSTEM OPERATION AND CONTROL	38	24	1
169T1D5610	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	33	25	1
169T1D5610	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	38	24	1
169T1D5610	I5612	GENERATION AND MEASUREMENT OF HIGH VOLTA	37	31	1
169T1D5610	I5615	SIMULATION LABORATORY	30	57	1
169T1D5801	I0504	ADVANCED OPERATING SYSTEM	36	-1	0
169T1D5801	I0505	DATA WAREHOUSING AND DATA MINING	36	24	1
169T1D5801	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	24	1
169T1D5801	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	-1	0
169T1D5801	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	37	24	1
169T1D5801	I5803	DATABASE MANAGEMENT SYSTEMS	37	-1	0
169T1D5801	I5805	CSE LAB 1	37	55	1
169T1D5802	I0504	ADVANCED OPERATING SYSTEM	37	18	0
169T1D5802	I0505	DATA WAREHOUSING AND DATA MINING	36	24	1
169T1D5802	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	15	0
169T1D5802	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	12	0
169T1D5802	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	36	25	1
169T1D5802	I5803	DATABASE MANAGEMENT SYSTEMS	37	24	1
169T1D5802	I5805	CSE LAB 1	36	58	1
169T1D5803	I0504	ADVANCED OPERATING SYSTEM	38	36	1
169T1D5803	I0505	DATA WAREHOUSING AND DATA MINING	38	24	1
169T1D5803	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	27	1
169T1D5803	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	37	30	1
169T1D5803	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	38	26	1
169T1D5803	I5803	DATABASE MANAGEMENT SYSTEMS	36	46	1
169T1D5803	I5805	CSE LAB 1	38	57	1
169T1D5804	I0504	ADVANCED OPERATING SYSTEM	36	45	1
169T1D5804	I0505	DATA WAREHOUSING AND DATA MINING	37	18	0
169T1D5804	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	24	1
169T1D5804	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	37	28	1
169T1D5804	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	38	38	1
169T1D5804	I5803	DATABASE MANAGEMENT SYSTEMS	38	39	1
169T1D5804	I5805	CSE LAB 1	38	57	1
169T1D7201	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	32	-1	0
169T1D7201	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	-1	0
169T1D7201	I5704	FRONT END VLSI DESIGN LABORATORY	36	51	1
169T1D7201	I6801	DIGITAL SYSTEM DESIGN	33	-1	0
169T1D7201	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	-1	0
169T1D7201	I6803	CMOS ANALOG IC DESIGN	33	-1	0

Htno	Subcode	Subname	Internal	External	credits
169T1D7201	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	35	-1	0
169T1D7202	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	30	1
169T1D7202	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	30	1
169T1D7202	I5704	FRONT END VLSI DESIGN LABORATORY	37	54	1
169T1D7202	I6801	DIGITAL SYSTEM DESIGN	35	5	0
169T1D7202	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	36	4	0
169T1D7202	I6803	CMOS ANALOG IC DESIGN	33	3	0
169T1D7202	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	32	1	0
169T1D8701	I2201	ADVANCED MATHEMATICS	35	0	0
169T1D8701	I8701	THEORY OF ELASTICITY	36	10	0
169T1D8701	I8702	MATRIX ANALYSIS OF STRUCTURES	35	14	0
169T1D8701	I8703	STRUCTURAL DYNAMICS	33	14	0
169T1D8701	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	16	0
169T1D8701	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	9	0
169T1D8701	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	33	50	1
169T1D8703	I2201	ADVANCED MATHEMATICS	35	24	1
169T1D8703	I8701	THEORY OF ELASTICITY	39	13	0
169T1D8703	I8702	MATRIX ANALYSIS OF STRUCTURES	37	20	0
169T1D8703	I8703	STRUCTURAL DYNAMICS	36	24	1
169T1D8703	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	26	1
169T1D8703	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	26	1
169T1D8703	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	34	53	1
169T1D8704	I2201	ADVANCED MATHEMATICS	37	28	1
169T1D8704	I8701	THEORY OF ELASTICITY	35	37	1
169T1D8704	I8702	MATRIX ANALYSIS OF STRUCTURES	36	24	1
169T1D8704	I8703	STRUCTURAL DYNAMICS	37	24	1
169T1D8704	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	37	28	1
169T1D8704	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	52	1
169T1D8704	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	35	54	1
169T1D8705	I2201	ADVANCED MATHEMATICS	34	5	0
169T1D8705	I8701	THEORY OF ELASTICITY	33	4	0
169T1D8705	I8702	MATRIX ANALYSIS OF STRUCTURES	33	8	0
169T1D8705	I8703	STRUCTURAL DYNAMICS	34	-1	0
169T1D8705	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	-1	0
169T1D8705	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	-1	0
169T1D8705	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	35	52	1
169T1D8706	I2201	ADVANCED MATHEMATICS	35	-1	0
169T1D8706	I8701	THEORY OF ELASTICITY	36	-1	0
169T1D8706	I8702	MATRIX ANALYSIS OF STRUCTURES	36	-1	0
169T1D8706	I8703	STRUCTURAL DYNAMICS	37	-1	0
169T1D8706	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	35	-1	0
169T1D8706	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	-1	0
169T1D8706	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	34	-1	0
169T1D8707	I2201	ADVANCED MATHEMATICS	36	0	0
169T1D8707	I8701	THEORY OF ELASTICITY	37	16	0
169T1D8707	I8702	MATRIX ANALYSIS OF STRUCTURES	38	11	0
169T1D8707	I8703	STRUCTURAL DYNAMICS	38	0	0
169T1D8707	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	10	0
169T1D8707	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	26	1
169T1D8707	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	33	-1	0
169T1D8708	I2201	ADVANCED MATHEMATICS	36	10	0



Htno	Subcode	Subname	Internal	External	credits
169T1D8708	I8701	THEORY OF ELASTICITY	36	19	0
169T1D8708	I8702	MATRIX ANALYSIS OF STRUCTURES	35	16	0
169T1D8708	I8703	STRUCTURAL DYNAMICS	35	14	0
169T1D8708	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	37	12	0
169T1D8708	I8707	REPAIR AND REHABILITATION OF STRUCTURES	37	10	0
169T1D8708	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	34	53	1
169T1D8709	I2201	ADVANCED MATHEMATICS	37	30	1
169T1D8709	I8701	THEORY OF ELASTICITY	39	29	1
169T1D8709	I8702	MATRIX ANALYSIS OF STRUCTURES	38	19	0
169T1D8709	I8703	STRUCTURAL DYNAMICS	37	24	1
169T1D8709	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	38	27	1
169T1D8709	I8707	REPAIR AND REHABILITATION OF STRUCTURES	37	26	1
169T1D8709	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	37	54	1
169T1D8710	I2201	ADVANCED MATHEMATICS	36	0	0
169T1D8710	I8701	THEORY OF ELASTICITY	37	19	0
169T1D8710	I8702	MATRIX ANALYSIS OF STRUCTURES	34	5	0
169T1D8710	I8703	STRUCTURAL DYNAMICS	30	0	0
169T1D8710	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	35	12	0
169T1D8710	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	12	0
169T1D8710	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	33	50	1
169T1D8711	I2201	ADVANCED MATHEMATICS	36	28	1
169T1D8711	I8701	THEORY OF ELASTICITY	35	29	1
169T1D8711	I8702	MATRIX ANALYSIS OF STRUCTURES	38	17	0
169T1D8711	I8703	STRUCTURAL DYNAMICS	37	14	0
169T1D8711	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	37	14	0
169T1D8711	I8707	REPAIR AND REHABILITATION OF STRUCTURES	33	26	1
169T1D8711	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	36	55	1
169T1D8712	I2201	ADVANCED MATHEMATICS	37	12	0
169T1D8712	I8701	THEORY OF ELASTICITY	38	20	0
169T1D8712	I8702	MATRIX ANALYSIS OF STRUCTURES	37	14	0
169T1D8712	I8703	STRUCTURAL DYNAMICS	34	18	0
169T1D8712	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	37	14	0
169T1D8712	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	29	1
169T1D8712	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	37	55	1
169T1D8713	I2201	ADVANCED MATHEMATICS	36	0	0
169T1D8713	I8701	THEORY OF ELASTICITY	36	-1	0
169T1D8713	I8702	MATRIX ANALYSIS OF STRUCTURES	36	-1	0
169T1D8713	I8703	STRUCTURAL DYNAMICS	30	-1	0
169T1D8713	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	35	-1	0
169T1D8713	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	-1	0
169T1D8713	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	36	53	1
169T1D8714	I2201	ADVANCED MATHEMATICS	29	-1	0
169T1D8714	I8701	THEORY OF ELASTICITY	31	-1	0
169T1D8714	I8702	MATRIX ANALYSIS OF STRUCTURES	32	-1	0
169T1D8714	I8703	STRUCTURAL DYNAMICS	29	-1	0
169T1D8714	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	29	-1	0
169T1D8714	I8707	REPAIR AND REHABILITATION OF STRUCTURES	31	-1	0
169T1D8714	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	32	-1	0

Note:1)For Recounting/Revaluation do the Online registration and send the total amount through online transfer

2)Take Seperate DD for the Challenge Valuation

**\*\*Note:1)**For Recounting/Revaluation/Challenge By Revaluation Apply through Online([www.jntukresults.edu.in](http://www.jntukresults.edu.in))

**\*\*NOTE:2** [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **26-07-2017**]

**\*\*NOTE:3** [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation ]

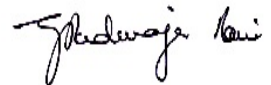
**\*\*NOTE:**

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject. ]

Date:05-07-2017



Controller of Examinations